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U.S. No. 09/822,052

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Applicants: Pillay, et al.

Serial No.: 09/822,052

Filed: 03/30/2001

Title: CIRCUITS AND METHODS FOR DEBUGGING AN EMBEDDED
PROCESSOR AND SYSTEMS USING THE SAME

Confirm No.: 8450

Art Unit: 2184

Examiner: Gabriel L. Chu

Mail Stop: Amendment
Commissioner For Patents
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Technology Center 2100

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AMENDMENT RESPONSE TO OFFICE ACTION

In response to the Official Action mailed November 26, 2003 (Paper No. 4), Applicants thank the Examiner for allowance of Claims 8 and 10-13. However, Applicants respectfully request that the Examiner reconsider the rejection of remaining claims in view of the following amendments and the comments as set forth below.

Amendments to the Specification begin on Page 2.

Amendments to the Claims are reflected in the listing of claims, which begins on Page 3 of this paper.

Remarks/Arguments begin on Page 7 of this paper.